

PATENT
W&B Docket No.: INF 2071-US
OC Docket No.: INFN/0040
Express Mail No.: EV335472220US

ABSTRACT OF THE DISCLOSURE

The invention relates to a method for producing an antifuse structure in a substrate, a conductive region and a nonconductive region adjoining the latter being formed in the substrate, so that an edge of the conductive region is produced, a dielectric layer being deposited in such a way that it covers at least a part of the edge.

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